

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L39	43	imag\$4 with chip\$1 and uniform\$4 near6 transist\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 07:27
L38	0	imag\$4 with chip\$1 same uniform\$4 near6 transist\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 07:27
L37	0	imag\$4 with chip\$1 same "uniform\$4" near6 transist\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 07:26
L36	4	chip\$1 same "same" adj polarit\$5 same imag\$5	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/10 07:26
L25	1140	imag\$4 with chip\$1 and "same" with transist\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 07:26
L35	232	chip\$1 same "same" adj polarit\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 07:25
L29	256	chip\$1 same "same" adj conductivit\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 07:20
L34	103	advantage\$3 with "same" adj conductivit\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 07:13
L33	0	preferred with "same" adj conductivit\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 07:13
L32	0	preferred with "same" adj conductivit\$5	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/10 07:13
L31	82	chip\$1 same "same" adj conductivit\$5	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/10 07:12
L30	253	29 not 16 not 14	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:56
L16	20	imag\$4 near2 chip\$1 same conductivit\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:55
L28	176	26 not 8	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:50
L26	177	imag\$4 near3 chip\$1 and "same" near2 transist\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:50
L24	8	imag\$4 with chip\$1 and "same" with transist\$5	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/10 06:49
L23	2	imag\$4 with chip\$1 and "same" with conductivit\$5	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/10 06:48
L22	0	imag\$4 near2 chip\$1 and "same" adj conductivit\$5	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/10 06:47
L17	24	imag\$4 near2 chip\$1 and "same" adj conductivit\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:47

L21	9	imag\$4 near2 chip\$1 and "only" near2 conductivit\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:46
L20	0	imag\$4 near2 chip\$1 and "only" adj conductivit\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:46
L19	21	17 not 16	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:44
L18	17	16 not 17	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:44
S2	13	imag\$4 near2 chip\$1 same (nmos or n adj mos)	USPAT	OR	OFF	2006/01/10 06:42
L15	25	14 not 8	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:35
L14	74	(mos or transistor\$1 or fet\$1 or mosfet\$1) same "same" near2 conductivit\$5 same imag\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:35
L13	7	(mos or transistor\$1 or fet\$1 or mosfet\$1) with only with conductivit\$5 same imag\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:35
L8	64	(mos or transistor\$1 or fet\$1 or mosfet\$1) with "same" with conductivit\$5 same imag\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:35
L11	99	(mos or transistor\$1 or fet\$1 or mosfet\$1) with one with conductivit\$5 same imag\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:34
L12	83	11 not 8	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:33
L10	177	(mos or transistor\$1 or fet\$1 or mosfet\$1) with "same" with conductivit\$5 same chip\$1	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:29
L9	62	(mos or transistor\$1 or fet\$1 or mosfet\$1) with "same" with conductivit\$5 same imag\$5	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/10 06:25
L7	1	(mos or transistor\$1 or fet\$1 or mosfet\$1) with "same" with conductivit\$5 same imag\$5 with chip\$1	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:10
S93	1204	(nmos or n near2 mos or n near2 channel or n near2 transistor\$1 or n near2 fet\$1) with image\$5	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:08
S91	929	(nmos or n near2 mos or n near2 channel or n near2 transistor\$1 or n near2 fet\$1) with faster\$1	US-PGPUB; USPAT	OR	OFF	2006/01/10 06:07
L6	63	(nmos or n near2 mos or n near2 channel or n near2 transistor\$1 or n near2 fet\$1) with faster\$1	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/10 06:07
L5	287	(nmos or n near2 mos or n near2 channel or n near2 transistor\$1 or n near2 fet\$1) with image\$5	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/10 05:36

S95	1	("6268614").PN.	USPAT	OR	OFF	2006/01/09 15:08
S94	1141	S93 not S86 not S89	US-PGPUB; USPAT	OR	OFF	2006/01/09 15:08
S92	40	(nmos or n near2 mos or n near2 channel or n near2 transistor\$1 or n near2 fet\$1) with faster\$1 same chip\$1	US-PGPUB; USPAT	OR	OFF	2006/01/09 09:20
S86	27	imag\$4 near2 chip\$1 same (nmos or n near2 mos or n near2 channel or n near2 transistor\$1 or n near2 fet\$1)	US-PGPUB; USPAT	OR	OFF	2006/01/09 09:13
S90	111	S89 not S86	US-PGPUB; USPAT	OR	OFF	2006/01/09 08:44
S89	138	imag\$4 same (nmos or n near2 mos or n near2 channel or n near2 transistor\$1 or n near2 fet\$1) same chip\$1	US-PGPUB; USPAT	OR	OFF	2006/01/09 08:44
S88	2911	imag\$4 same (nmos or n near2 mos or n near2 channel or n near2 transistor\$1 or n near2 fet\$1)	US-PGPUB; USPAT	OR	OFF	2006/01/09 08:44
S87	12	imag\$4 near2 chip\$1 same (nmos or n near2 mos or n near2 channel or n near2 transistor\$1 or n near2 fet\$1)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/09 08:41
S85	384	substrate with electric adj conductor\$1	USPAT	OR	OFF	2006/01/09 08:12
S84	792	(257/291-293).CCLS.	USPAT	OR	OFF	2006/01/09 08:11
S1	69	inokuma-kazuyuki\$.in. fujii-toshiya\$.in. yamaguchi-takumi\$.in.	US-PGPUB; USPAT	OR	OFF	2006/01/09 08:11